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Group Art Unit: 2185
Examiner: Unknown

Atty. Dkt. No. 5732-00300

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below:

5/22/02
Date

Kevin L. Daffer

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MAY 29 2002

Technology Center 2100

Sir:

Applicant requests consideration of ☒ the references listed on the attached Form PTO-1449 and/or ☒ the additional information identified below in paragraph 3. A copy of each reference listed on the Form PTO-1449 is enclosed.

1. This Information Disclosure Statement is submitted:

- a. ☐ within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);
- ☐ within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application;
- ☒ before the mailing date of a first Office Action on the merits; or
- ☐ before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.
- b. ☐ after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: ☐ the certification of paragraph 2 below is provided, or ☐ a fee of \$180.00 is enclosed.

- c. ☐ after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.

2. It is hereby certified:

☐ that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Statement, or

☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three months prior to the filing of the Statement.

3. ☒ Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:

SN 09/928,536 entitled "Vertically-Stacked, Field-Programmable, Nonvolatile Memory and Method of Fabrication," and SN 10/010,643 entitled "Three-Dimensional, Mask-Programmed Read Only Memory"

4. For each non-English language reference listed on the attached Form PTO-1449:

☐ reference is made to an English language translation submitted herewith, and/or

☐ reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or

☐ reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or

☐ reference is made to the concise explanation contained in the specification of the present application at page(s) _____, and/or

☐ reference is made to the concise explanation set forth below:

5. ☐ Applicant also offers the following comments for the Examiner's consideration:

6. ☐ Also enclosed is a copy of a foreign search report citing these references.

7. ☐ The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure

Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.

8. ☐ Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

If any required fees are missing, the Commissioner is authorized to charge said fees to Conley, Rose & Tayon, P.C. Deposit Account No. 50-1505/5732-00300.

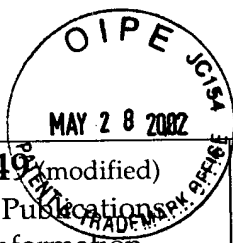
Respectfully submitted,



Kevin L. Daffer
Reg. No. 34,146
Attorney for Applicant(s)

CONLEY, ROSE & TAYON, P.C.
P. O. Box 398
Austin, Texas 78767
(512) 476-1400

Date: 5/22/02



Form PTO-1449 (modified)
List of Patents and Publications
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)

ATTY. DKT. NO. 5732-00300
APPLICANT: Verma et al.
FILING DATE: February 19, 2002

SERIAL NO. 10/080,036
GROUP: 2185

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	B1	Re. 36,229	6/1999	Cady			
	B2	5,221,642	6/1993	Burns			
	B3	5,236,117	8/1993	Roane et al.			
	B4	5,279,029	1/1994	Burns			
	B5	5,367,766	11/1994	Burns et al.			
	B6	5,369,056	11/1994	Burns et al.			
	B7	5,369,058	11/1994	Burns et al.			
	B8	5,371,866	12/1994	Cady			
	B9	5,377,077	12/1994	Burns			
	B10	5,420,751	5/1995	Burns			
	B11	5,446,620	8/1995	Burns et al.			
	B12	5,448,450	9/1995	Burns			
	B13	5,455,740	10/1995	Burns			
	B14	5,479,318	12/1995	Burns			
	B15	5,484,959	1/1996	Burns			
	B16	5,493,476	2/1996	Burns			
	B17	5,498,906	3/1996	Roane et al.			
	B18	5,499,160	3/1996	Burns			
	B19	5,528,075	6/1996	Burns			
	B20	5,543,664	8/1996	Burns			
	B21	5,550,711	8/1996	Burns et al.			
	B22	5,552,963	9/1996	Burns			
	B23	5,561,591	10/1996	Burns			
	B24	5,566,051	10/1996	Burns			
	B25	5,572,065	11/1996	Burns			
	B26	5,581,121	12/1996	Burns et al.			
	B27	5,585,668	12/1996	Burns			

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EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)			ATTY. DKT. NO. 5732-00300 APPLICANT: Verma et al. FILING DATE: February 19, 2002		SERIAL NO. 10/080,036 GROUP: 2185		
U.S. PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	B28	5,586,009	12/1996	Burns			
	B29	5,588,205	12/1996	Roane			
	B30	5,592,364	1/1997	Roane			
	B31	5,605,592	2/1997	Burns			
	B32	5,615,475	4/1997	Burns			
	B33	5,631,193	5/1997	Burns			
	B34	5,644,161	7/1997	Burns			
	B35	5,654,877	8/1997	Burns			
	B36	5,702,985	12/1997	Burns			
	B37	5,778,522	7/1998	Burns			
	B38	5,783,464	7/1998	Burns			
	B39	5,801,437	9/1998	Burns			
	B40	5,804,870	9/1998	Burns			
	B41	5,828,125	10/1998	Burns			
	B42	5,843,807	12/1998	Burns			
	B43	5,864,175	1/1999	Burns			
	B44	5,895,232	4/1999	Burns			
	B45	5,945,732	8/1999	Burns			
	B46	5,960,539	10/1999	Burns			
	B47	5,978,227	11/1999	Burns			
	B48	6,025,642	2/2000	Burns			
	B49	6,034,882	3/2000	Johnson et al.			
	B50	6,049,123	4/2000	Burns			
	B51	6,168,970	1/2001	Burns			
	B52	6,190,939	2/2001	Burns			
	B53	6,194,247	2/2001	Burns et al.			
	B54	6,205,654	3/2001	Burns			

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U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	B55	6,252,305	6/2001	Lin et al.			
	B56	6,258,626	7/2001	Wang et al.			
	B57	6,274,930	8/2001	Vaiyapuri et al.			
	B58	6,278,181	8/2001	Maley			
	B59	6,282,210	8/2001	Rapport et al.			
	B60	6,288,907	9/2001	Burns			
	B61	6,310,392	10/2001	Burns			

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	B62	"Multi-Chip & Stacked Leadframe Packages," Amkor Technology, Inc., 5 pgs.
	B63	"MultiMedia Card (MMC)," Amkor Technology, Inc., 4 pgs.
	B64	"Four Semiconductor Manufacturers Agree to Unified Specifications for Stacked Chip Scale Packages," July 5, 1999 Mitsubishi Semiconductors Press Release, 3 pgs.
	B65	"Samsung Develops 512Mb Flash Memory," January 21, 2001 Digital Photography Review, 2 pgs.
	B66	Data Sheet for MMC Multi Media Card, Siliconware Precision Industries Co., Ltd.
	B67	"Toshiba Develops 8Mb Low-Power SRAM Device and Industry's First 8Mb SRAM/64Mb NOR Flash Stacked MCP Memory Device," March 9, 2000 Toshiba Press Release, 3 pgs.
	B68	Edwards, "Hitachi moves to multi-chip to get EEPROM on top," July 18, 2001 Electronic Times, 3 pgs.

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